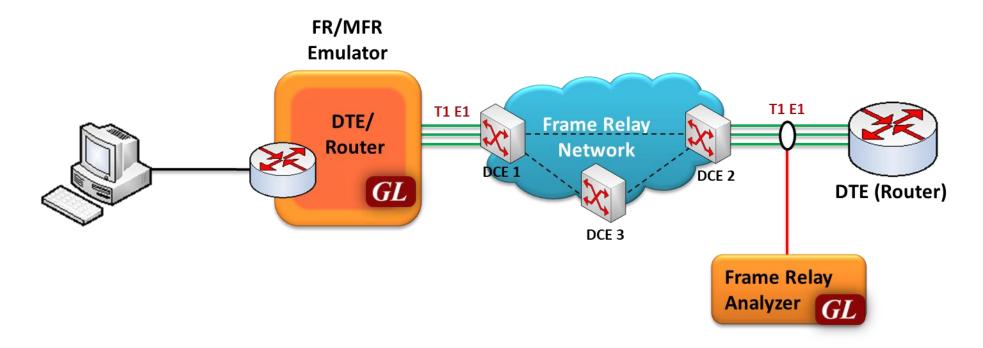
Multi-Link Frame Relay Emulator (MFR)

(FR and MFR Simulation)

GL Communications Inc.

818 West Diamond Avenue - Third Floor, Gaithersburg, MD 20878 Phone: (301) 670-4784 Fax: (301) 670-9187 Email: <u>info@gl.com</u> Website: https://www.gl.com

MFR Emulator Working Principle



- Multi-Link Frame Relay Emulation (MFR) software based on client-server architecture over GL's field proven T1 E1 hardware platforms
- The software acts as a Frame Relay (FR)-MFR Data Terminal Equipment (DTE)/Router and generates traffic in compliance with frame relay fragmentation & reassembly models i.e., UNI (DTE-DCE) NNI (DCE peers) & end-to-end fragmentation over multiple virtual circuits



Hardware Platforms



tProbe[™] - Portable USB based T1 E1 VF FXO FXS and Serial Datacom Analyzer



Dual T1 E1 Express (PCIe) Board



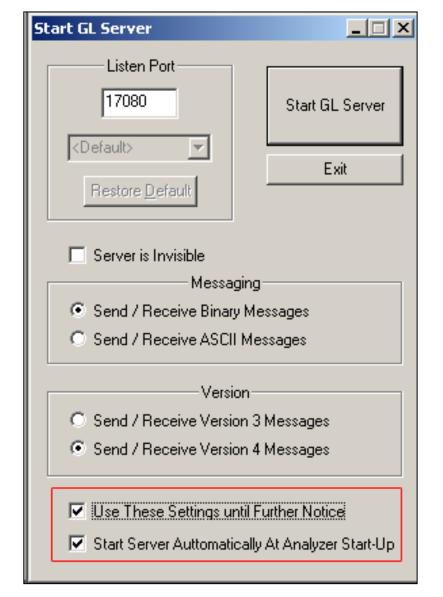
Quad / Octal T1 E1 PCIe Card

tScan16™ with 16-port T1 E1 Breakout Box



Connecting to the Server

- Listen Port: This is the TCP/IP port on which the server should listen for incoming connection requests from clients. By default, the Listen port for T1 card is set as **17080** and for E1, it is set as **17090**
- Send / Receive Binary Messages: Indicates that the server is to communicate with clients using binary messages
- Send / Receive ASCII Messages: Indicates that the server is to communicate with clients using ASCII (text-based) messages
- Send / Receive Version 3 Messages: Indicates that the server is to communicate with clients using version 3 messages
- Send / Receive Version 4 Messages: Indicates that the server is to communicate with clients using version 4 messages
- Use these settings Until Further Notice: This option to use the current configuration settings as default settings at analyzer startup
- Start Server Automatically At analyzer Startup: It will start the WCS server at analyzer startup by default





Simulating Frame Relay Links

- Various links (of any bandwidth varying from 64Kbps to n*64Kpbs or sub channels) can be added in FR Simulation
- Two or more than two timeslots can be grouped to constitute a Hyper-channel

M	FR Emulator - FF	R Simulation	- Untitled		_	×
<u>F</u> i	le <u>A</u> ction <u>S</u> i	mulation <u>H</u>	<u>H</u> elp			
Se	erver Connection	Status (Links #1:15	•
	Link View Traffi	c VC Statistic	cs Tx/Rx Verific	aition		1
	Link Name	Action	Status			
	#1:15	Close	Up			
	#1:610	Close	Up			- 1
	#1:1115	Close	Up			- 1
	Fragment Si	tation ——	on	Close		



Link Configuration

- Provides Frame Fragmentation configuration adhering to FRF.12 standard for traffic generation on selected FR links
- Supports two types of fragmentation: UNI NNI Fragmentation and End-to-End Fragmentation on a FR link
- Allows to configure the bandwidth using flags

Link Config Impairments Statistics	HDLC Statistics
Fragmentation	
Fragment Size 256	✓ Flags between Hdlc frames 100
UNI NNI Fragmentation	
C End to End Fragmentation	



Simulating MFR Bundle

- Allows to create a virtual interface referred as 'bundle' interface
- An MFR bundle can consist of multiple physical links of the same type or physical links of different types
- Data sent through this channel will be distributed among all the links
- It is used to derive larger bandwidth pipe by aggregating smaller bandwidth pipes e.g. from multiple T1s or E1s

MFR Emulator - MFR Simulation ·	Untitled	_	×
<u>File Action Simulation H</u> e	р		
Server Connection Status (
MFR Bundles Status	Link View Traffic VC Statistics Tx/Rx Verification Bundle Config & Stat	istics	
1 UP			 1
	Link Name Action Status		
	#1:110 Close Up #1:1120 Close Up		- 1
	#1:2130 Close Up		
	Add Delete Open Close		
	Link Config Impairments Statistics HDLC Statistics		
	Fragmentation		
	Fragment Size 256 Flags between Hdlc frames 1	100	
	UNI NNI Fragmentation		
	C End to End Fragmentation		
Bundle ID 2			
Add Delete			
Open Close			



Impairments

DELETE FRAME INSERT FRAME

DELETE BYTES INSERT BYTES DUPLICATE FRAME

CRC FRAME

AND OR XOR

- Enable the user to intentionally introduce errors in data transmission.
- Impairments can be applied at different levels, i.e.
 - Impair all packets sent over a Physical Link
 - Impair frames on a particular Virtual Channel [VC may be on a physical link or on the MFR bundle]
 - Impair frames on a particular Aggregated
 Virtual Channel
 - Impair all packets on the MFR bundle

Link Config Impairments Statistics HDLC Statistics
Impairment Type DELETE FRAME Options Impairment Duration Frame count 1 Byte Offset 1 Skip Before Impair 1 Oelay 250 msec Apply Sync All Links



Pattern/File Traffic

- The source of the traffic is either a file or a repetitive pattern as defined by the user
- Traffic type can be used for end-to-end testing of the link
- The verification process will provide results such as how many frames are received and out of which how many have been matched successfully with configured pattern, similarly, how many frames modified etc.
- BERT test can also be conducted using various pre-defined patterns or a user defined pattern file

MFR Emulator - FR Simulation - Untitled <u>File Action Simulation H</u> elp	FR - '	VCs on a Selected Link		×
Server Connection Status			Links #1:15 #1:15	•
Link View Traffic VC Statistics Tx/Rx Verificait	tion		#1:610	
Pattern/File Traffic Network Traffic PacketChe	eck Traffic		#1:1115	
DLCI - 1 DLCI - 2 DLCI - 3	RX params Sink Type Sink Param Order MSB Start 0			

MFR Emulator	- MFR Simulat	tion - Unt	$^{\text{titled}}$ MFR - VCs on a Selected Bundle $^{-}$ $^{-}$ \times
<u>File</u> <u>Action</u>	Simulation	<u>H</u> elp	
Server Connec	tion Status (>	
MFR Bundles	Status		Link View Traffic VC Statistics Tx/Rx Verification Bundle Config & Statistics
1 2	UP UP		Pattern/File Traffic Network Traffic
			Add Vc Delete Vc DLCI - 1 DLCI - 2 DLCI - 3 TX params Source Type SEQNUM Source Parameters Order MSB Length 4 Start 0 Increment 1 Start 0 Increment 1 Start 0 Increment 1 Add Vc Delete Vc Add Vc Delete Vc Add Vc Delete Vc Sink Params Sink Type SEQNUM Sink Parameters Order MSB Length 4 Start 0 Increment 1

TxRx Verification

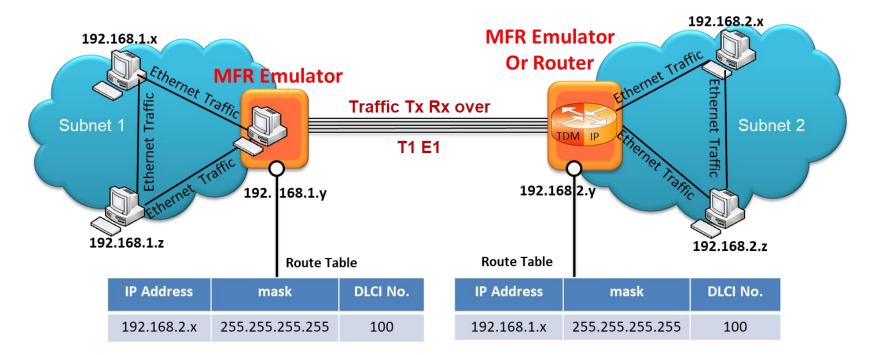
Link View Traffic VC Statistics Tx/Rx Verificaition Bundle Config & Statistics									
Reset									
VC	Tx Cnt	Rx Cnt	Matched Cnt	Modified Cnt	Inserted Cnt	Deleted Cnt	Bert Status		
1	1592	1395	1286	2	0	2	N/A		
2	1590	1395	1286	5	0	5	N/A		
3	1590	1394	1284	3	0	3	N/A		
Total	4772	4184	3856	10	0	10			

- The results of the verification for each of the added VCs are available in Tx/Rx Verification
- The statistics include:
 - The number of VCs created
 - > The number of frames transmitted successfully
 - The number of frames received successfully
 - > If a received frame is verified successfully, then it will be included in "Matched" Frame Count
 - > If a received frame does not match, it will be included in the "Modified" Frame Count
 - > If the frame is lost then it will be included in "Deleted" Frame Count
 - > If extra frames have been received which were not expected then they will be included in Inserted Frame Count



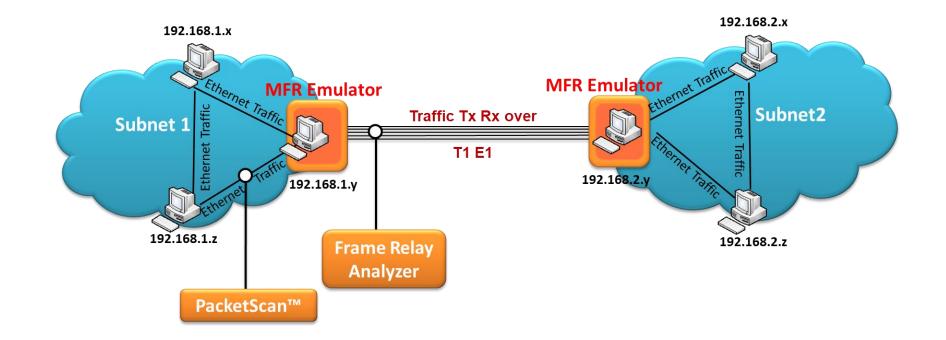
Network Traffic - MFR Emulator as a Router

- Allows user to setup routing table by configuring IP Address and Mask
- Once configured, the emulator forwards the IP packets which match routing criteria over MFR links
- Emulator responds to all ARP requests whose IP addresses present in routing table
- The image shows two networks, Subnet1 and Subnet2, connected through T1 E1 lines using MFR Emulator that is configured to work as router





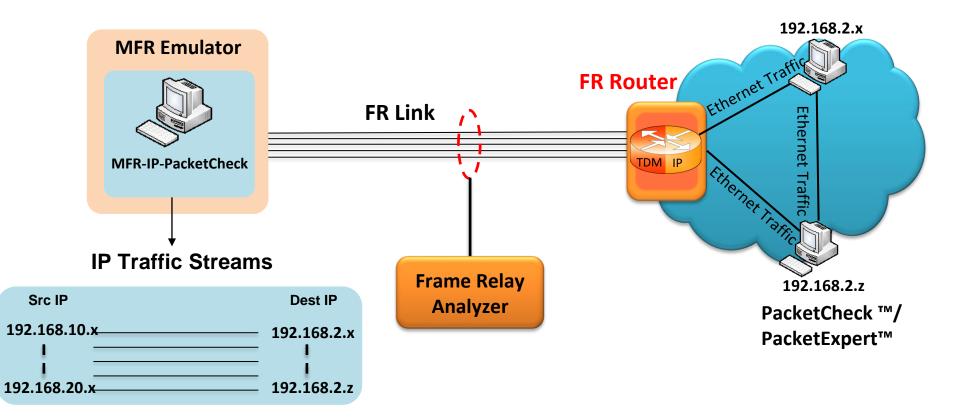
Network Traffic - MFR Emulator as a Bridge



- When the MFR Emulator is configured to act as bridge between two networks, all ARP and traffic received from the network is encapsulated as bridged IP and streamed over T1 E1 links
- The Emulator on another network removes bridging header, converts to Ethernet and streams to the destination



PacketCheck[™] Traffic



- Allows IP traffic generation and reception over FR links
- Multiple IP traffic streams can be generated and processed over multiple VCs created within the FR links
- VCs can be configured to encapsulate the IP packets with desired custom headers to emulate various protocols
- MFR-IP-PacketCheck traffic is used to generate and receive IP packet streams to and from a FR router
- FR Router shall be tested for routing the received packets to the proper destination



PacketCheck™ Traffic Configuration

	Simulation H n Status 🔿	lelp							Links #1	:031 💌
k View Traf	fic VC Statistic	s T×/R× Verificaition								
attern/File Tr	affic Network 1	raffic PacketCheck T	raffic							
			1							
Route Config			[[([[[1
Stream Id		Src MAC Address	Src MAC Mask	Dest MAC Address	Dest MAC Mask	ETH Type	Src IP Address	Src IP Mask	Dest IP Address	Dest IP Mask
1	Test1	00-00-00-22-00-22	FF-FF-FF-FF-FF	00-00-00-11-00-11	FF-FF-FF-FF-FF	0000	0.0.0.0	0.0.0.0	0.0.0.0	255.255.255.
2	Test2	00-00-00-22-00-22	FF-FF-FF-FF-FF	00-00-00-11-00-12	FF-FF-FF-FF-FF	0000	0.0.0.0	0.0.00	0.0.0.0	255.255.255.
3	Test3	00-00-00-11-11-11	FF-FF-FF-FF-FF	00-00-00-22-22-22	FF-FF-FF-FF-FF	0800	192.168.12.1	255.255.255.255	192.168.12.2	255.255.255.
4	Test4	00-00-00-20-20-20	FF-FF-FF-FF-FF	00-00-00-10-10-10	FF-FF-FF-FF-FF	0800	192.168.12.10	255.255.255.255	192.168.12.11	255.255.255.
5	Test5	00-20-20-20-20-20	FF-FF-FF-FF-FF	00-01-01-01-01-01	FF-FF-FF-FF-FF	0800	192.168.12.12	255.255.255.255	192.168.12.20	255.255.255.
6	Test6	00-20-20-20-32-32	FF-FF-FF-FF-FF	00-01-01-01-01-22	FF-FF-FF-FF-FF	0800	192.168.1.12	255.255.255.255	192.168.1.20	255.255.255.
<										
Load Pack	etCheck Config	Add Route D	elete Route Record to	o File						

- Supports Layer 2, Layer 3 and Layer 4 Bert packets to send out via Route table
- Allows to create multiple Routes and multiple VCs on the FR links
- Each Route will have its own route criteria and an assigned VC
- Packets that pass through the defined criteria of a route, will be transmitted on the VC assigned to that route



Linked Statistics

 Provides important statistics information for the selected link such as such as the Number of frames transmitted, Received frames, Octets Transmitted, and Octets Received

M	MFR Emulator - FR Simulation - MFRTest									
<u>F</u> i	le <u>A</u> ction <u>S</u> ir	mulation <u>H</u>	elp							
Se	Server Connection Status									
	Link View Traffic	: VC Statistics	s Tx/Rx Verifica	ition						
	Link Name Action Status									
	#1:023	Close	Up							
	Add	Delete	Open		Ca					
					30					
	Link Config Ir	npairments St	atistics HDLC S	tatistics						
	Number of Frames Transmitted 2545226 Reset									
	Number of Frames Received 2435479									
	Number of (Octets Transmil	ted 384838171	.2						
	Number o	of Octets Recei	ved 363395271	.3						
			,							



HDLC Statistics

 Errors that occur during transmission / reception like the Tx Under/Over Runs, Rx Under/Over Runs, number of FR packets with bad FCS, and number of packets with Frame Errors is recorded in the HDLC Statistics fields

MF	IFR Emulator - FR Simulation - Untitled								
File	e Action Sir	mulation H	lelp						
Sei	rver Connection	Status 😑							
		1	1						
	ink View Traffic	VC Statistic	s Tx/Rx Verifica	aition					
	Link Name	Action	Status						
	#1:023	Close	Up						
	#2:023	Close	Up						
	Add	Delete	Open	Close					
	Link Config Im	nairmente St	tatistics HDLC S	Itatistics					
1		ipainients [3							
	Tx Unde	r/Over Runs	0	Reset					
	Rx Unde	er/Over Runs	0						
		-	,						
	CRC	Error Frames	3402						
	Frame	Error Frames	0						
Ш									



VC Statistics

erver Co	nnection Status	•						Links #1:131	•
Link Viev	v Traffic VC S	itatistics Tx/Rx	Verificaition						
Reset									
٧C	Tx Frames	Tx Frags	Tx Octets	Rx Frames	Rx Frags	Rx Octets	Lost Frags		
200	3346	0	5019000	3414	0	5121000	0		
300	3345	0	230845	3415	0	235838	0		
400	3345	0	5017500	3415	0	5122500	0		
500	3344	0	5016000	3413	0	5119500	0		
600	3344	0	5016000	3413	0	5119500	0		
Total	16724	0	20299345	17070	0	20718338	0		

• The statistics for each of the added VCs are available and these include number of Transmitted and received frames, Fragments, Octets, and Lost fragments



MFR Simulation in Command Line Interface

	in the fact
FrameRelay_E1.gls - GLClient	
Elle Edit View Connect Script Log User Help	
D 📽 🖬 🕺 🐵 📾 🚭 🚌 🎎 🗅 📽 🔚 🕮 📾 🐞 👺 🗚 🚦 🎖	
OK	
inform task 3 "CREATE VC HC #1:131 DLCI 1 FRAG FORMAT END TO END FRAGSIZE 256";	
inform task 3 'Tx: HC #1:131 DLCI 1 CONT FIXLEN 1500 SEQNUM MSB4";	
inform task 3 "START TX HC #1:131 DLCI 1"; OK	-
query task 3;	
Task 3:	
Simulation=Frame Relay, Total FR Links=1, Active FR Links=1, Selected Link=1:131, Link Status=Active,	
====== HDLC Stats ======, Tx Octets=9159516, Tx Frames=35502, Rx Octets=0, Rx Frames=0, Tx Over/U	Inder
Runs=0, Rx Over/Under Runs=0, CRC Error Count=0,	
====== Virtual Channel Stats ======, Number of VC's on FR Link: '1:131'=1,	
VC 1, DLCI=1, Tx Frames=5917, Tx Frags=35502, Rx Frames=0, Rx Frags=0, Lost Frags=0, Received cou	nt=0,
Matched count=0, Modified count=0, Inserted count=0, Deleted count=0	
јок	•
//There should be tragmentation with B=T,E=U for first tragment,	
<pre>//B=0,E=0 for in between fragments and B=0, E=1 for last fragment.</pre>	
ave deals INVEREmulatorE1:TuR-II	
run task "MFREmulatorE1:TxRx"; inform task 1 "SIMULATION FR";	
inform task 1 "HC #1:131 FLAGS 100";	
//inform task 1 'TS #1:131 FLAGS 100';	
//inform task 1 "SC #1:131:18 FLAGS 100";	
inform task 1 "ACTIVATE HC #1:131";	
//inform task 1 "ACTIVATE TS #1:131";	
//inform task 1 "ACTIVATE SC #1:131:18";	
	1000
inform task 1 "CREATE VC HC #1:131 DLCI 1 FRAG FORMAT END TO END FRAGSIZE 500";	1000
//inform task 1 "CREATE VC TS #1:131 DLCI 1 FRAG FORMAT END TO END FRAGSIZE 500";	
//inform task 1 "CREATE VC TS #1:131 DLCI 1 FRAG FORMAT END TO END FRAGSIZE 500"; //inform task 1 "CREATE VC SC #1:131:18 DLCI 1 FRAG FORMAT END TO END FRAGSIZE 500";	
//inform task 1 "CREATE VC TS #1:131 DLCI 1 FRAG FORMAT END TO END FRAGSIZE 500"; //inform task 1 "CREATE VC SC #1:131:18 DLCI 1 FRAG FORMAT END TO END FRAGSIZE 500"; inform task 1 "Tx: HC #1:131 DLCI 1 FRAMES 10 FIXLEN 1500 SEQNUM MSB4";	
<pre>//inform task 1 "CREATE VC TS #1:131 DLCI 1 FRAG FORMAT END TO END FRAGSIZE 500"; //inform task 1 "CREATE VC SC #1:131:18 DLCI 1 FRAG FORMAT END TO END FRAGSIZE 500"; inform task 1 "Tx: HC #1:131 DLCI 1 FRAMES 10 FIXLEN 1500 SEQNUM MSB4"; //inform task 1 "Tx: TS #1:131 DLCI 1 FRAMES 10 FIXLEN 1500 SEQNUM MSB4";</pre>	-
//inform task 1 "CREATE VC TS #1:131 DLCI 1 FRAG FORMAT END TO END FRAGSIZE 500"; //inform task 1 "CREATE VC SC #1:131:18 DLCI 1 FRAG FORMAT END TO END FRAGSIZE 500"; inform task 1 "Tx: HC #1:131 DLCI 1 FRAMES 10 FIXLEN 1500 SEQNUM MSB4";	



Thank you

